DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Name	: K.SUMATHI
Designation	: AP Grade-II Date of Joining: 13.07.2011
Email	: sumathi.ece@sairamit.edu.in
Qualification	:M.E.,(Ph.D)
Specialization	: APPLIED ELECTRONICS
Research Interest	: RF TRANSMITTER DESIGN USING CMOS TECHNOLOGY

Experience in years : **Teaching** UG [13.3] PG [ ] **Industry** [ ]

No. of Workshop/Conferences/ FDP attended : Workshop [14] Conferences [06] FDP [09]

No. of Workshop/Conferences/ FDP Organized : Workshop [ ] Conferences [ ] FDP [ ]

Professional Membership	ISTE MEMBER, IEANG MEMBER
Publications	National : Nil
	International: 06
	Book :Nil
Research Funded Projects	-
Patents	-

Achievements	-
Any Other Information	1. Received NPTEL **ELITE** Certificate for the course "OUTCOME BASED PEDAGOGIC PRINCIPLES FOR EFFECTIVE TEACHING". 2. Received NPTEL **ELITE** Certificate for the course "DEVELOPING SOFT SKILLS AND PERSONALITY". 3. Received NPTEL Certificate for the course "BASICS OF SOFTWARE DEFINED RADIO".

List of Publications:-

1. Published a paper "**Performance study of 4:1 Multiplexer CMOS Logic Structures** " during Feb 11th to 13th 2016 held at University college of Engineering(ICRTCCM'2016),Tindivanam, ISSN:0974-5572, vol.9,No.1(2016)

2. Published a paper **"Dust Motes For Border Survelilance"** on March 10th 2017 held at International Journal of Advanced Research Methodology in Engg., & Technology,(IER-MAET'17)Salem Tamilnadu,ISBN 978-1-63535-324-2,PAGES(119-123)

3. Published a paper "**Study and Design of RF CMOS Oscillator** "in Asian Journal of Applied Science.

Workshop / Conferences / FDP Attended:-

1. Attended one day **workshop** on "**Computer Networking** ", in Anna University , on 14/10/2006.

2. Attended one day **workshop** on "**Virtual Instrumentation** ",conducted by National Instruments(India) Bangalore held on 25th September 2004 at Shree Motilal Kanhaiyalal Fomra Institute of Technology.

3. Attended one day **workshop** on "**A Refresher course on VLSI Technologies**", held on 3rd September 2005 at Sakthi Mariamman Engineering College.

4. Attended National level one day **workshop** on "**Embedded System Design**", held on 2nd March 2012 at St.Joseph's College of Engineering

5. Attended five days **FDP** titled "**VLSI Design"** held on 25th - 29th November 2013 at Dhanalakshmi College of Engineering.

6. Attended five days **FDP** titled "**VLSI Design"** held on 25th - 29th November 2013 at Dhanalakshmi College of Engineering.

7. Attended National level Short-Term Training Program on "**MSP430 launchepad Based Embedded System Design**", held on 27th September 2014 at VIT University.

8. Attended two days **workshop** titled **"Hands on TRAing in NS2**", held on 6th & 7th November 2014 at Dhanalakshmi College of Engineering.

9. Attended seven days **FDP** for the subject titled "**EC2354-VLSI Design**" held from 8th - 14th November 2014 at Karpaga Vinayaga College of Engineering & Technology.

10. Attended National level one day **workshop** on "**How to write Research Papers for Referred Journals**", held on 24th January 2015 at Sri Sairam Insititute of Technology.

11. Attended National level one day **workshop** on "**Brain SENSE**", held on 28th March 2015 organized by the School of Electronics Engineering ,VIT University.

12. Attended one day **workshop** titled **"Research Methodology**", held on 18th July 2015 at Sathyabama University.

13. Attended two days **workshop** titled **"Circuit Simulation Using EDA tools"** conducted by Live wire held on 4th & 5th August 2015 at Sri Sairam Insititute of Technology.

14. Attended two days Hands-on **workshop** titled **"System design on Zynq Using SDSoC"** conducted by CoreEL Technologies(I) Pvt. Ltd and Indian Institute Technology, Madras held on 2nd & 3rd January 2016.

15. Attended two days National Seminar titled **"IPR Awareness & Research Methodologies"** held on 27th & 28th May 2016 at Karpaga Vinayaga College of Engineering & Technology.

16. Attended UKIERI sponsored National level three days **workshop** on "**Advanced Topics in Signal Processing , RF & Wireless Communication**, held on 13th -15th June 2016 at IIITD & M, Kancheepuram.

17. Attended Faculty Enablement Program on "**A step to outcome based Accreditation**", held on 4th April 2015 at Sri Sairam Insititute of Technology.

18. Attended one week FUP on "**Digital system design using Verilog HDL** "
Organized by C-DAC Hyderabed, held from 5th may -9th May 2015.

19. Attended ISTE sponsored FDP on "**EMF theory and transmission lines**", held on 25th and 26th June 2015 at Sri Sairam Insititute of Technology.

20. Attended two days FDP on "**Modern teaching and learning methodologies for competency development** "organized by department of ECE , held on 3rd and 4th May 2017 at Jeppiar Maamallan Engineering College.

21. Attended two days FDP on "**Time management** "organized by ITC Academy , held on 8th and 9th May 2017 at SA Engineering College.

22. Attended 6 days FDP on "**RF and Microwave and circuit design using Ansys Electro magnetic suite** "held on 25th to 30th October 2017 at Sathyabama University.

23. Attended 7 days FDP on "**BE8254-Basic Electrical and Indtrumentation Engineering**" at Jeppiaar Maamallan Engineering College, Sriperumpudur from 22nd to 29th November 2017.

24. Published a paper "**Reducing Power Consumption using TEM & Distributed Detection of Defective Modules**" in International Conference on Electrical ,Electronics,Computers,Communications,Mechnical and Computing(EECCMC) -2018 during 28th & 29th January 2018, organized by Priyadarshini Engineering College,Vellore District, Tamil Nadu,INDIA.

24. Published a paper "**Smart Driving License Authentication System**" in International Conference on Innovation in Science and Research(ICISER'18) during 23rd & 24th March 2018, organized by New Prince Shri Bhavani College of Engg., and Technology.

25. Published a paper "**Low Cost Microstrip Patch Antenna**" in 3rd International Conference on Innovation & Emerging Trends in Engg., and Technology during 9th & 10th March 2018, organized by Panimalar Institute of Technology with CSI,IETE,IEEE ICTACT andSAE,INDIA.

26. Attended the TECH WORKSHOP ON REDHAT(OPEN SOURCE) TECHNOLOGY & INDUSTRY ADAPTATION held on 2nd March 2018, at Sri Sairam Institute of Technology.