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SAI RAM INSTITUTE OF TECHNOLOGY

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Name : N.RAJESWARI

Designation : AP – G II Date of Joining: 18.06.2008

Email : raji.ece@sairamit.edu.in

Qualification : M.Tech

Specialization : VLSI DESIGN

Research Interest : VLSI, Digital Signal Processing

Experience in years : Teaching UG PG Industry

No. of Workshop/Conferences/
FDP attended : Workshop Conferences FDP

No. of Workshop/Conferences/
FDP Organized : Workshop Conferences FDP



Professional Membership	ISTE
Publications	National :Nil
	International:Nil
	Book :Nil
Research Funded Projects	NIL
Patents	NIL

Achievements	<ul style="list-style-type: none"> • Obtained 100% result in Communication Theory • Obtained 100% result in Embedded Systems
Any Other Information	-

List of Publications: NIL

List of Patent :NIL

.Workshop / Conferences / FDP Attended:-

1. Attended a workshop on “Advanced control systems “at Sri Sai Ram Engineering College.
2. Attended a FDP on “VLSI Simulation” at Kings college of engineering- Punalkulam,Pudukottai.
3. Attended a workshop on “VLSI circuit simulation and layout” by Cadence groups in Chennai.
4. Attended 5 days workshop in” High Impact Teaching Methodology” by “Wipro Mission 10X”in Sri Sai Ram Engineering College.