



Sri

SAI RAM INSTITUTE OF TECHNOLOGY

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Name : M. Pown
 Designation : AP – III
 Email : pown.ece@sairamit.edu.in
 Qualification : M. E., Ph.D
 Specialization : VLSI Design
 Research Interest : Semiconductor Devices and Circuits
 Experience in years : **Teaching** UG PG **Industry**
 No. of Workshop/Conferences/
 FDP attended : Workshop Conferences FDP
 No. of Workshop/Conferences/
 FDP Organized : Workshop Conferences FDP



Professional Membership	<ul style="list-style-type: none"> • IEEE (98101854) • IETE (AM-235828) • IAENG (237751)
Publications	National : -
	International: 8
	Book : -
Research Funded Projects	-
Patents	-
Achievements	-
Any Other Information	-

List of Publications:-

1. **M. Pown**, S. Sandeep and B. Lakshmi, "Investigation of Homo and Hetero-Junction Double-Gate Tunnel-FET-Based Adiabatic Inverter Circuits", IETE Journal of Research, pp. 1-9, Dec 2020. [Indexed in: SCI, Scopus. **IF: 1.125**]
2. **M. Pown** and B. Lakshmi, "Investigation of Radiation Hardened TFET SRAM cell for Mitigation of Single Event Upset", IEEE Journal of the Electron Devices Society, vol. 8, June 2020. [Indexed in: SCI, Scopus. **IF: 2.555**]
3. **M. Pown** and B. Lakshmi, "Stability analysis of homo and hetero-junction based TFET SRAMs", International Journal of Scientific and Technology Research, vol. 9, no. 11, pp. 102-108, June 2020.
4. **M. Pown**, B. Lakshmi, "Performance Analysis of InAs and GaSb-InAs Based Independent Gate Tunnel Field Effect Transistor RF Mixers", Journal of Computational Electronics (Springer) vol. 16, no. 3, pp. 676-684, Sep 2017. [Indexed in: SCI, Scopus. **IF: 1.532**]
5. S. Poorvasha, **M. Pown**, B. Lakshmi, "Tunnel Field Effect Transistors for Digital and Analog Applications: A Review", Indian Journal of Science and Technology, vol. 10, no. 13, pp. 1-7, April 2017.
6. **M. Pown** and B. Lakshmi, "Investigation of f_t and f_{max} in Si and Si_{1-x}Ge_x based single and dual material double-gate Tunnel FETs for RF applications", Advances in Natural Sciences: Nanoscience and Nanotechnology (IOP Science), vol. 7, No.2, pp.1-7, April 2016. [**Scopus CiteScore: 4.9**]
7. Viral Gokani, Roop Narayan Goswami, **M. Pown**, B. Lakshmi, "Impact of Underlap Variation on the Analog Parameters of High-K Gate Dielectric based Double Gate Tunnel FETs", International Journal of Applied Engineering Research, Vol-10, No-92, pp. 268-273, 2015.
8. D. Selvathi, **M. Pown**, and S. Manjula, "Design and Analysis of UWB Down-Conversion Mixer with Linearization Techniques", WSEAS Transactions on Circuits and Systems, vol. 13, pp. 202-207, July 2014.

Workshop / Conferences / FDP Attended:-

1. Participated in the 19th Hands-on Training on "Photovoltaics and Micro and Nano Characterization Techniques" conducted at IISc, Bangalore, from 3-12 February 2015.
2. Participated in the INUP Familiarization Workshop on "Nanofabrication Technologies" conducted at IISc, Bangalore, 28-30 January 2015.
3. Participated workshop on Applications of Microcontroller in Tamilnadu College of Engineering (TCE), Coimbatore.
4. Participated workshop on Embedded System in TCE, Coimbatore.

FDP:

1. Attended Three day online FDP on eSim organized by VIT, Chennai, 2020.
2. Attended one week FDP on Python Programming organized by Geethanjali Institute of Science and Technology, Nellore, AP, 2020.
3. Attended one week online FDP on "Signals, Systems and Transform Techniques" organized by Maturi Venkata Subba Rao Engineering College, Hyderabad, November 2021.
4. Attended Refresher/Induction programme on "FPGA for Low Power IoT" organized by SRM Valliammai Engineering College, Chennai, December 2021.

International Conference

1. **M. Pown**, B. Lakshmi, "Effect of the Geometrical Parameters on f_t in Si and Si_{1-x}Gex Dual Material Double-Gate TFETs", IEEE International Conference on Electronics and Communication Systems (ICECS 2016), pp. 239-243.
2. Viral Gokani, Roop Narayan Goswami, **M. Pown**, B. Lakshmi, "Impact of Underlap Variation on the Analog Parameters of High-K Gate Dielectric based Double Gate Tunnel FETs", Proceedings of International Conference on Researches in Science, Management and Engineering (ICRSME 2016), 18-19 February 2016.
3. D. Selvathi and **M. Pown**, "Design of Band Pass Filter using Active Inductor for RF receiver Front-end", IEEE International Conference on Communication and Network Technologies (ICCNT), pp. 296-301, December 2014.